

JAN 18 2007

## ABSTRACT

An adaptive multi-bit delta and sigma-delta modulation and demodulation technique, wherein a one-bit modulator generates a binary output signal from an analog input signal and a multi-bit adapter for generating a scaling signal for scaling a step-size of the sigma-delta modulator.

**IN THE ABSTRACT**

Please add an Abstract at page 17 of the application using the Abstract in the replacement page submitted herewith.